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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application / Conf. No.	Unknown /
				Filing Date	November 18, 2003
				First Named Inventor	Tony K. Ngai
				Art Unit	Unknown
				Examiner Name	Unknown
				Attorney Docket Number	X-1363 US
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U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- 3,995,261	11-30-76	Goldberg	
		US- 4,020,469	04-26-77	Manning	
		US- 4,700,187	10-13-87	Furtek	
		US- 4,899,067	02-06-90	So et al.	
		US- 5,153,880	10-06-92	Owen	
		US- 5,255,227	10-19-93	Haeffele	
		US- 5,459,342	10-17-95	Nogami et al.	
		US- 5,485,102	01-16-96	Cliff et al.	
		US- 5,498,975	03-12-96	Cliff et al.	
		US- 5,513,144	04-30-96	O'Toole	
		US- 5,592,102	01-07-97	Lane et al.	
		US- 5,742,556	04-21-98	Tavrow et al.	
		US- 5,764,577	06-09-98	Johnston et al.	
		US- 5,777,887	07-07-98	Marple et al.	
		US- 5,889,413	03-30-99	Bauer	
		US- 5,914,616	06-22-99	Young et al.	
		US- 6,166,559	12-26-00	McClintock et al.	
		US- 6,167,558	12-26-00	Trimberger	
		US- 6,344,755 B1	02-05-02	Reddy et al.	
		US- 6,356,514 B1	03-12-02	Wells et al.	

[illegible]

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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<b>Examiner Name</b>	Unknown
<b>Attorney Docket Number</b>	X-1363 US

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Attorney Docket Number	X-1363 US

**OTHER -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		John Emmert et al.; "Dynamic Fault Tolerance in FPGAs via Partial Reconfiguration"; Annual IEEE Symposium on Field-Programmable Custom Computing Machines; 17 April 2000; pp. 165-174.	
		John M. Emmert et al.; "Incremental Routing in FPGAs"; ASIC Conference 1998. Proceedings, Eleventh Annual IEEE International; Rochester, NY; 13-16 September 1998; pp. 217-221.	
		Xilinx, Inc.; "Virtex-II Pro Platform FPGA Handbook"; published October 14, 2002; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 19-71.	

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